

CLAIMS

We claim:

- 1 1. A method of tuning a system comprising:
2 tuning a receiver to a first frequency;
3 receiving a first field of video associated with the first frequency;
4 tuning a receiver to a second frequency;
5 receiving a second field of video associated with the second frequency, wherein
6 the first field of video and the second field of video are adjacent in time;
- Sub 2 2. The method of claim 1, wherein the step of tuning the receiver to a second frequency
2 further comprises tuning the receiver to the second frequency during a vertical
3 blanking interval.
- 1 3. The method of claim 1 further comprising:
2 providing a second frequency indicator to the receiver prior to the step of tuning
3 the receiver to a second frequency.
- 1 4. The method of claim 3, wherein the step of providing includes providing the second
2 frequency indicator in less than approximately 1.2 milliseconds.
- Sub 3 5. The method of claim 1 further comprising the steps of :
2 displaying the first field;
3 tuning the receiver to the first frequency after the step of receiving the second
4 field;
5 receiving a third field associated with the first frequency;
6 displaying the third field, wherein the first field and the third field are adjacent
7 frames of a common video image.

- 1 6. The method of claim 1, wherein the first and second fields of video are adjacent when
2 no fields of video are transmitted at the second frequency after a last data of the
3 first field of video and before the first data of the second field of video.

- sub 2 7. The method of claim 1, wherein the step of tuning the receiver to a second frequency
occurs during a vertical blanking interval.

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- 1 8. A method of providing video, the method comprising:
 - 2 tuning a receiver to a first frequency;
 - 3 receiving a first field of video associated with the first frequency;
 - 4 tuning a receiver to a second frequency;
 - 5 receiving a second field of video associated with the second frequency, wherein
 - 6 the first field of video and the second field of video are adjacent in time;
 - 7 tuning the receiver to the first frequency;
 - 8 receiving a third field of video associated with the first frequency;
 - 9 displaying an image based upon the first field at a first location of a display
 - 10 device;
 - 11 displaying an image based upon the second field at a second location of a display
 - 12 device, wherein the first location and the second location are substantially
 - 13 mutually exclusive; and
 - 14 displaying an image based upon the third field at the first location of the display
 - 15 device to provide a full motion video sequence.

1 9. A method of displaying video, the method comprising:
2 alternating reception of a first field set and a second field set at a common
3 receiver, wherein the first field set is associated with a first frequency, and
4 the second frame set is associated with a second frequency; and
5 simultaneously displaying the first field set and the second field set as full motion
6 video.

Sub a6 } 10. The method of claim 9, wherein the step of alternating includes alternating reception
2 of a first field set and a second field set at a common receiver in approximately
3 1.2 milliseconds.

3 } 11. The method of claim 9, wherein the step of simultaneously displaying includes
2 simultaneously displaying the first field set and the second frame set as full
3 motion video on a single display device.

1 12. The method of claim 9, wherein the step of simultaneously displaying includes
2 simultaneously displaying the first field set and the second frame set as full
3 motion video on different display devices.

1 13. A system comprising:
 2 a first prelude register having an output;
 3 a first control register coupled to the output of the first prelude register, the first
 4 control register having an output ;
 5 a second prelude register having an output;
 6 a second control register coupled to the output of the first prelude register, the
 7 second control register having an output ;
 8 a first oscillator coupled to the output of the first control register, the first
 9 oscillator to provide an output signal;
 10 a second oscillator coupled to the output of the second control register, the second
 11 oscillator to provide an output signal ;
 12 a first mixer to coupled to an antenna and to receive the output of the first
 13 oscillator and to provide an output signal; and
 14 a second mixer coupled to receive the output signal from the first mixer and the
 15 output signal from the second oscillator, and to provide an output signal.

1 14. The system of claim 13 further comprising:
 2 an analog-to-digital converter coupled to receive the output signal from the
 3 second mixer and providing a digital output; and
 4 a video decoder coupled to receive the digital output from the analog-to-digital
 5 converter, and to provide a digital video output.

1 15. The system of claim 14 further comprising:
 2 a display engine coupled to receive the digital video output to a display device.

1 16. The system of claim 13, further comprising:
 2 a memory coupled to receive the digital video output.

1 17. The system of claim 13, wherein the memory includes a cache memory.

1 18. The system of claim 13, wherein the memory includes a video memory.

1 19. The system of claim 13, wherein the memory includes a system memory.

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- 1 20. A system comprising:
 - 2 a first register having an output;
 - 3 a second register having an output;
 - 4 a third register having an output;
 - 5 a fourth register having an output;
 - 6 a first multiplexor having a first input coupled to the output of the first register, a
7 second input coupled to the output of the second register, a select input,
8 and an output;
 - 9 a second multiplexor having a first input coupled to the output of the third
10 register, a second input coupled to the output of the fourth register, a select
11 input, and an output;
 - 12 a first oscillator coupled to the output of the first multiplexor, the first oscillator to
13 provide an output signal;
 - 14 a second oscillator coupled to the output of the second multiplexor, the second
15 oscillator to provide an output signal ;
 - 16 a first mixer to coupled to an antenna and to receive the output of the first
17 oscillator and to provide an output signal; and
 - 18 a second mixer coupled to receive the output signal from the first mixer and the
19 output signal from the second oscillator, and to provide an output signal.